



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Zamir Ahmad and Jeffrey F. Wong

Title: REDUCED DELAY POWER FAIL-SAFE CIRCUIT

Application No.: 10/716,690

Filed: November 19, 2003

Examiner: Don P. Le

Group Art Unit: 2819

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**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

Dear Sir:

In accordance with the clarification of 37 C.F.R. § 1.704(c)(10) set forth in 1247 OG 111 (June 26, 2001), the submission of this paper after a Notice of Allowance has been mailed is *not* considered a “failure to engage in reasonable efforts” to conclude processing or examination of an application and would *not* result in reduction of a patent term adjustment pursuant to 37 C.F.R. § 1.704(c)(10). *Simply stated, this paper is not to be treated as a “paper submitted after the mailing of a Notice of Allowance” under 37 C.F.R. § 1.704(c)(10).*

This is in response to the statements made by the Examiner in the Examiner’s Statement of Reasons for Allowance mailed to Applicants on May 10, 2005.

Applicants respond as follows:

Although Applicants believe that the claims are allowable over the art of record, the Examiner’s Reasons for Allowance do not coincide with the allowed claims. Applicants do not acquiesce in additional limitations included in the Examiner’s Reasons for Allowance.

**CERTIFICATE OF MAILING OR TRANSMISSION**

I hereby certify that, on the date shown below, this correspondence is being

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Respectfully submitted,



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